

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-7. (Canceled)

Claim 8. (Currently Amended) A method for producing discrete semiconductor devices, comprising the steps of:

forming a plurality of sets of die bond pads and wire bond pads ~~by fastening with~~ electrically conductive metal sheets at specified positions on the back of an insulating sheet and making apertures in the insulating sheet on the metal sheets,

packaging the discrete semiconductor elements, said packaging step comprising fastening the back of the discrete semiconductor elements on the die bond pads and electrically connecting the electrodes of the discrete semiconductor elements and the wire bond pads, the die bond pads and the wire bond pads being adopted to be mounted on a motherboard,

sealing the plurality of discrete semiconductor elements installed on the insulating sheet with an integral sealing resin by sealing the packaging surface of the insulating sheet with the resin, and

dividing the sealing resin into the discrete semiconductor devices by cutting off the sealing resin around the discrete semiconductor elements.

Claim 9. (Previously Presented) A method as claimed in claim 8, wherein

the step of packaging further comprises fastening a back side electrode of the discrete semiconductor device onto the corresponding die bond pad to electrically connect the die bond pad and the back side electrode.

Claim 10. (Previously Presented) A method as claimed in claim 8, wherein
the dividing step comprises cutting off the sealing resin around a plurality of discrete semiconductor elements grouped as a single body such that at least one of the discrete semiconductor devices comprises a plurality of the discrete semiconductor elements sealed with the integral resin.

Claim 11. (Currently Amended) A method for producing a discrete semiconductor device, comprising the steps of:

packaging a plurality of discrete semiconductor elements, said packaging step comprising fastening the back of the discrete semiconductor elements onto an electrically conductive metal sheet and connecting electrically an electrode of each discrete semiconductor element to a specified position of the metal sheet,

sealing the packaging surface of the metal sheet with an integral sealing resin,
cutting off into the metal sheet by ~~cutting therein~~ from the back to turn the metal sheet, on which the discrete semiconductor elements are fastened, into die bond pads and wire bond pads which are arranged at intervals, and

dividing the discrete semiconductor devices by cutting off the sealing resin around the discrete semiconductor elements.

Claim 12. (Previously Presented) A method as claimed in claim 11, wherein
the packaging step comprises fastening the back electrode of the discrete semiconductor elements onto the metal sheet and electrically connecting the metal sheet and the back electrode.

Claim 13. (Previously Presented) A method as claimed in claim 11, wherein the dividing step comprises cutting off the sealing resin around a plurality of discrete semiconductor elements grouped as a single body, to divide the discrete semiconductor devices each carrying the plurality of discrete semiconductor elements being sealed with the integral resin.

Claim 14. (Previously Presented) A method as claimed in claim 11, wherein the step of cutting off the metal sheet comprises cutting off the metal sheet such that the die bond pads and/or the wire bond pads connected to the plurality of discrete semiconductor elements become an integral body, and the dividing step comprises cutting off the sealing resin around the discrete semiconductor elements which are formed so that the die bond pads and/or the wire bond pads connected to the plurality of discrete semiconductor elements become an integral body, thereby to obtain the discrete semiconductor device wherein the plurality of discrete semiconductor elements which share the die bond pads and/or the wire bond pads in common are sealed with the integral resin.